

### **1. Dr. V. Lakshminarasimhan,**

Professor of Computer science,  
Botswana, South Africa.

Prof. V. Lakshmi Narasimhan is a full professor of Computer Science at the University of Botswana. He has published over 380 papers in the areas of Software Engineering, Information security and Information Engineering and related case studies extensively. In particular, his research interests are in computer architecture, parallel and distributed computing, software testing, data mining, Software process, asset management systems and Standards, and information management & fusion and their extensive case studies and performance evaluations. His papers have appeared in such archival journals as the various IEEE Transactions and IEE Proceedings. He has also been the technical chair of two other conferences and has been on the technical panel of over 80 leading International Conferences. Lakshmi has won competitive research funding to the tune of US\$8 million. He has consulted to a number of industries and educational institutions on various IT and Software Engineering projects, including Boeing Aerospace and DoD. Prof. Narasimhan is a Senior Member of the IEEE & ACM, Fellow of ACS, IEAust and IEE (UK). He is a Technical Member of various Standards bodies such as, ISO, ANSI and IEEE. Lakshmi is an IEEE Distinguished Visitor and an ACM Distinguished Speaker.

### **2. Dr. V. Kamakoti,**

Professor, Department of Computer Science and Engineering,  
IIT (M), Chennai.

Research Interests: Software aspects of VLSI design, Cluster computing, High-performance computing, Algorithms, Data structures, Computational geometry

Awards:

- IBM Faculty Award, 2016
- DRDO Academy Excellence Award, 2013
- IITM Young Faculty Recognition Award (YFRA), 2007

**(Current) Sponsored Projects:**



Microprocessor Development Programme (Funding Agency: MEITY)  
Indigenous 64-bit processor design (Funding Agency: DRDO)  
Graphics processing unit based parallel processing system for radar signal processing (Funding Agency: DRDO)  
A2PG for processor architectures and processor-based SOC verification - Phase II (Funding Agency: INTEL-ASIA)  
Synthesizable models of digital design using the India VLSI design flow (Funding Agency: DRDO)

**3. Dr. G. Ramasubramanian,**  
Department of Computer science,  
National Institute of Technology, Trichy.

#### Areas of Interest

Multi-core Computer Architecture, Advanced Digital Design, Reconfigurable computing, Processor Design, Software for Embedded Applications, Hardware for Security Systems, GPU Computing, IoT Architectures



**4. Dr. Anandhakumar,**  
Professor and Head,  
Department of Computer Science and Engineering,  
MIT, Chennai.

Qualification : B.E. (ECE), M.E. (CSE), Ph.D.  
E-Mail ID : anandh@mitindia.edu  
Area Of Interest : Networks, Digital Image processing, Artificial Intelligence,  
Computer Vision, Robotics, Multimedia Networks,  
Multimedia Data Structures, Artificial Neural Networks

International Journals	37
International conferences	42
National Journals	2



National Conferences	13
<b>Research Guidance</b>	
Ph.D. Awarded	12
Doing Research	10

### 5. Dr. T. ArunKumar Thangavelu,(IOT)

Professor, Department of Computer science,  
VIT, Vellore.

He did his UG & PG in Bharathidasan University, Trichy and also did his Doctoral Degree in PSG College of Technology

He worked as an Asst. Prof. in Sri Krishna College of Engineering and Technology, Coimbatore.

He handled multiple responsibilities related to software quality assurance, consultant project management and regular project operations leading towards effective completion of project in Logic Version Technologies, Mat Systems and Hisco (I) Ltd.,

He did research in National Aerospace Laboratories, Bangalore in Wireless Research Center.

He published more than 100 International & National Journals.

He wrote the books named “Distributed Systems- Concepts and Design” & “Hybrid Artificial Intelligence Heuristics and Clustering Algorithm for Combinatorial Asymmetric Travelling Salesman Problem.

He is the Chief Editor of International Journal of Research and Reviews in Information Technology

He is the Chairperson and Convener in International / National Conferences

He is the Reviewer for International Symposium on Knowledge Communication and Conferences

He is the Technical Reviewer for International Journal of Computer Science and Information Security

He is the Member of International Federation of Systems Research, Austria

He is the Visiting Professor for Eastern University, Sri Lanka for period of three years.

He was the Program Manager in School of Computing Science, VIT

He is the Advisor for Curriculum Development Committee for VIT – Vellore, Sastra University - Tanjore and NGM College, Coimbatore, PSG Krishnammal College, Coimbatore

He is the Board of studies member for Computer Science for Sri Krishna College of Engineering and Technology, Coimbatore and Annamalai University,Chidambaram

He handled Refresher Courses and Faculty Development Programme in Computer Science Department of Bharathiar University, Abdul Rahman University, VIT University and Annamalai University, TamilNadu

He guided students from various countries related to Research and Projects

He is the Country Representative Nomination for International society of precision agriculture



He was the Coordinator for conduct of first VIT MEE Online exam during 2011 and 2012 throughout India

He is the Technical Co-ordinator for VIT Academics Portal (V-TOP)

He guided 12 researchers in Doctoral degree and 2 more of them submitted and 6 of them ongoing.

Now He is & Sr. Professor in School of Science and Engineering, VIT University, Vellore.

Interested in researching / lecturing

Research Areas : Networking ( AdHoc , sensor, mobile, wireless, MANET, VANET)

Data mining (Prediction Models, Analysis of Data Quality) Software Engineering (Quality metrics, reverse engg)

**6. Mr. Ravi Shankar,**  
TCS, Chennai.